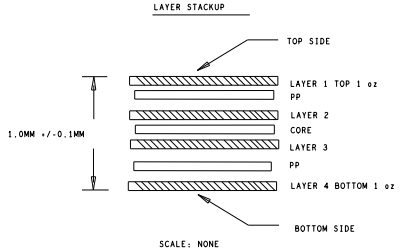
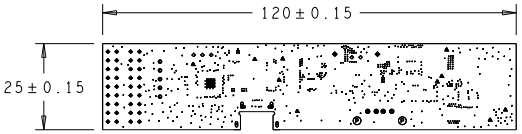
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1.0	INITIAL RELEASE	WW1634	
4	UPDATE U18 ROUTING, J1 FP	WW1639	



(X-SECTION VIEW /4 LAYER BOARD)



NOTES (UNLESS OTHERWISE SPECIFIED):

1. GENERAL
   - 1.0 ROHS COMPLIANT AND HF COMPLIANT.
   - 1.1 STANDARDS-ALL RAW CARDS MUST CONFORM TO IPC6011/IPC-6012 CLASS 2 USING THE MOST CURRENT REVISION LEVEL.
   - 1.2 FOR INSPECTION, BOARDS MUST MEET THE REQUIREMENTS OF THE LEVEL OF IPC-600 USING THE MOST CURRENT REVISION LEVEL.
2. MATERIAL
   - 2.0 NEMA GRADE FR-4 140 MIN.
   - 2.1 SEE LAYER STACKUP DETAIL FOR FINISHED COPPER LAYERS AND PCB THICKNESS.
   - 2.2 FLAMMABILITY RATING 94V-0.
3. ETCH
   - 3.0 ETCH TOLERANCE ON ALL FOIL FEATURES INCLUDING FINISHED TRACE WIDTH AND SPACING TOLERANCE IS +/-15% OF THE ORIGINAL ARTWORK/GERBER DESIGN.
   - 3.1 THE ARTWORK/GERBER DESIGN MINIMUM TRACE WIDTH IS 0.004 INCH AND THE MINIMUM COPPER TO COPPER SPACING IS 0.004 INCH.
4. LAYER STRUCTURE
   - 4.0 REFER TO DETAIL STACKUP FIGURE FOR LAYER STRUCTURE AND ASSOCIATED ARTWORK.
5. FINISH
   - 5.0 PLATE THRU WITH COPPER.
   - 5.1 FINISH SURFACE: ENIG. GOLD FINISH THICKNESS 0.05UM MIN.
   - 5.2 COPPER PLATING IN THRU-HOLES:0.001 INCH MIN.
6. SOLDERMASK
   - 6.1 SOLDERMASK TO BE GREEN.
   - 6.2 SOLDERMASK TO BE IN ACCORDANCE WITH IPC-SM-840C CLASS T.
7. DRILLING
   - 7.0 DIAMETERS IN DRILL TABLE ARE FINISHED HOLE SIZES +/-0.003 INCH TOL. UNLESS OTHERWISE SPECIFIED IN DRILL TABLE.
   - 7.1 TEARDROP ALLOWED ON ENTRY OF VIA ON EVERY TRACE LAYER.
   - 7.2 VIA SHOULD BE PLUGGED. DETAIL ALIGN WITH CYPRESS.
8. MARKING
   - 8.0 MARKING TO BE WHITE.
   - 8.1 MARKING NOT TO ENCROACH ON SURFACE MOUNT LANDS.
   - 8.2 FABRICATOR SHOULD ADD DATE CODE AND LOGO ON WHERE CYPRESS INDICATES.
9. VENDOR OPTIONS/REQUIREMENTS
   - 9.0 VENDORS MAY MAKE THE FOLLOWING CHANGES WITHOUT CYPRESS APPROVAL:
     - ADD TEARDROPS
   - 9.1 ANY OTHER CHANGES NEED WRITTEN APPROVAL FROM CYPRESS.
10. WARP/TWIST
    - 10.0 MAXIMUM WARP OR TWIST SHALL NOT EXCEED 0.5%.

CYPRESS

PROJECT NAME:	CCG3_POWER BANK
VERSION NO.:	REV4
RELEASE DATE:	2016WW39
LAYER:	DRILL
GERBER:	600-200xx-01

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
·	8.0	+3.0/-3.0	PLATED	614
*	12.0	+2.0/-2.0	PLATED	9
●	27.6	+3.1/-3.1	PLATED	4
▲	28.0	+3.0/-3.0	PLATED	10
▪	39.0	+3.0/-3.0	PLATED	36
⊕	40.0	+3.0/-3.0	PLATED	5
◇	40.0	+3.0/-3.0	PLATED	5
◆	82.7	+4.0/-4.0	PLATED	2
Ⓟ	90.6	+3.0/-3.0	PLATED	2
▯	60.0x28.0	+3.0/-3.0	PLATED	2
▭	54.0x26.0	+2.0/-2.0	NON-PLATED	2
▯	68.0x30.0	+2.0/-2.0	NON-PLATED	2

UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN INCHES	DRAWN	WW1639
TOLERANCES ON: ANGLES +/- 2°	CHECKED	
2 PL DECIMALS +/- .010	ENGRG	
3 PL DECIMALS +/- .005	ISSUED	
THIRD ANGLE PROJECTIONS		

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CYPRESS SEMICONDUCTOR Corp.

CCG3 POWER BANK

SIZE	REV:	DWG NO
	4	610-200xx-01
SCALE:		SHEET: 1 OF 1